

Electronic Acknowledgement Receipt

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Application Number:	10656801
International Application Number:	
Confirmation Number:	7106
Title of Invention:	Method and system for logic equivalence checking
First Named Inventor/Applicant Name:	Manish Pandey
Customer Number:	23639
Filer:	Peter C. Mei/Mary Zimmerman
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement (IDS) Filed	IDS_Form__SB_08a.pdf	756916	no	5
Warnings:					
Information:					
2	NPL Documents	Gupta_1A.pdf	3907920	no	49
Warnings:					
Information:					
3	NPL Documents	Gupta_1B.pdf	2371997	no	32
Warnings:					
Information:					
4	NPL Documents	Beatty.pdf	709490	no	6
Warnings:					
Information:					
5	NPL Documents	Burch.pdf	874305	no	7
Warnings:					
Information:					
6	NPL Documents	Kuehlmann.pdf	632795	no	6
Warnings:					
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7	NPL Documents	Kunz.pdf	501714	no	6
Warnings:					
Information:					
8	NPL Documents	Moskewicz.pdf	541060	no	6
Warnings:					
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9	NPL Documents	Fielding_1A.pdf	3115038	no	55
Warnings:					
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10	NPL Documents	Fielding_1B.pdf	3699280	no	64
Warnings:					
Information:					
11	NPL Documents	Fielding_1C.pdf	1890860	no	36
Warnings:					
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12	NPL Documents	Smith.pdf	1002693	no	12
Warnings:					
Information:					
13	NPL Documents	Kato.pdf	1724774	no	15
Warnings:					
Information:					
14	NPL Documents	Liskov.pdf	1618008	no	28
Warnings:					
Information:					
15	NPL Documents	Ebeling.pdf	193315	no	2
Warnings:					
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Total Files Size (in bytes):			23548337		

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